# Static Call Graph for image .\Objects\lab1.axf

#<CALLGRAPH># ARM Linker, 6100100: Last Updated: Wed Oct 10 10:06:55 2018

### Maximum Stack Usage = 24 bytes + Unknown(Functions without stacksize, Cycles, Untraceable Function Pointers)

### Call chain for Maximum Stack Depth:

\_\_rt\_entry\_main ⇒ main ⇒ configureOutput

### Functions with no stack information

* [\_\_user\_initial\_stackheap](#2nusc19)

### Mutually Recursive functions

* [NMI\_Handler](#1mrcu09)   ⇒   [NMI\_Handler](#1mrcu09)
* [HardFault\_Handler](#46r0co2)   ⇒   [HardFault\_Handler](#46r0co2)
* [SVC\_Handler](#2lwamvv)   ⇒   [SVC\_Handler](#2lwamvv)

### [PendSV\_Handler](#111kx3o)   ⇒   [PendSV\_Handler](#111kx3o) Function Pointers

* + [ADC0\_IRQHandler](#3l18frh) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [CMP0\_IRQHandler](#206ipza) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DAC0\_IRQHandler](#4k668n3) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DMA0\_IRQHandler](#2zbgiuw) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DMA1\_IRQHandler](#1egqt2p) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DMA2\_IRQHandler](#3ygebqi) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DMA3\_IRQHandler](#2dlolyb) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [DefaultISR](#sqyw64) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(.text)
  + [FTFA\_IRQHandler](#3cqmetx) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [HardFault\_Handler](#46r0co2) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [I2C0\_IRQHandler](#1rvwp1q) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [I2C1\_IRQHandler](#4bvk7pj) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [LLWU\_IRQHandler](#2r0uhxc) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [LPTMR0\_IRQHandler](#1664s55) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [LVD\_LVW\_IRQHandler](#3q5sasy) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [MCG\_IRQHandler](#25b2l0r) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [NMI\_Handler](#1mrcu09) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [PIT\_IRQHandler](#kgcv8k) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [PORTA\_IRQHandler](#34g0dwd) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [PORTD\_IRQHandler](#36ei31r) from main.o(.text.PORTD\_IRQHandler) referenced from startup\_mkl25z4.o(RESET)
  + [PendSV\_Handler](#111kx3o) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [RTC\_IRQHandler](#1jlao46) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [RTC\_Seconds\_IRQHandler](#43ky6rz) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [Reserved20\_IRQHandler](#2iq8gzs) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [Reserved39\_IRQHandler](#xvir7l) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [Reserved45\_IRQHandler](#3hv69ve) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [Reset\_Handler](#37m2jsg) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [SPI0\_IRQHandler](#1x0gk37) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [SPI1\_IRQHandler](#4h042r0) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [SVC\_Handler](#2lwamvv) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [SysTick\_Handler](#1ljsd9k) from systick.o(.text.SysTick\_Handler) referenced from startup\_mkl25z4.o(RESET)
  + [SystemInit](#45jfvxd) from system\_mkl25z4.o(.text.SystemInit) referenced from startup\_mkl25z4.o(.text)
  + [TPM0\_IRQHandler](#2w5ecyt) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [TPM1\_IRQHandler](#1baon6m) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [TPM2\_IRQHandler](#3vac5uf) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [TSI0\_IRQHandler](#2afmg28) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [UART0\_IRQHandler](#pkwqa1) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [UART1\_IRQHandler](#39kk8xu) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [UART2\_IRQHandler](#1opuj5n) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [USB0\_IRQHandler](#48pi1tg) from startup\_mkl25z4.o(.text) referenced from startup\_mkl25z4.o(RESET)
  + [\_\_main](#gjdgxs) from \_\_main.o(!!!main) referenced from startup\_mkl25z4.o(.text)

Global Symbols**\_\_main** (Thumb, 8 bytes, Stack size 0 bytes, \_\_main.o(!!!main))  
  
[Calls]

* + [>>](#30j0zll)   \_\_scatterload
  + [>>](#41mghml)   \_\_rt\_entry

**\_\_scatterload** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter))  
  
[Called By]

* + [>>](#gjdgxs)   \_\_main

**\_\_scatterload\_rt2** (Thumb, 52 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)  
  
[Calls]

* + [>>](#41mghml)   \_\_rt\_entry

**\_\_scatterload\_rt2\_thumb\_only** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)**\_\_scatterload\_null** (Thumb, 0 bytes, Stack size unknown bytes, \_\_scatter.o(!!!scatter), UNUSED)**\_\_scatterload\_copy** (Thumb, 26 bytes, Stack size unknown bytes, \_\_scatter\_copy.o(!!handler\_copy), UNUSED)  
  
[Calls]

* + [>>](#tyjcwt)   \_\_scatterload\_copy

[Called By]

* + [>>](#tyjcwt)   \_\_scatterload\_copy

**\_\_scatterload\_zeroinit** (Thumb, 28 bytes, Stack size unknown bytes, \_\_scatter\_zi.o(!!handler\_zi), UNUSED)**\_\_rt\_lib\_init** (Thumb, 0 bytes, Stack size unknown bytes, libinit.o(.ARM.Collect$$libinit$$00000000))  
  
[Called By]

* + [>>](#3fwokq0)   \_\_rt\_entry\_li

**\_\_rt\_lib\_init\_alloca\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000002E))**\_\_rt\_lib\_init\_argv\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000002C))**\_\_rt\_lib\_init\_atexit\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001B))**\_\_rt\_lib\_init\_clock\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000021))**\_\_rt\_lib\_init\_cpp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000032))**\_\_rt\_lib\_init\_exceptions\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000030))**\_\_rt\_lib\_init\_fp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000002))**\_\_rt\_lib\_init\_fp\_trap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001F))**\_\_rt\_lib\_init\_getenv\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000023))**\_\_rt\_lib\_init\_heap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000A))**\_\_rt\_lib\_init\_lc\_collate\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000011))**\_\_rt\_lib\_init\_lc\_ctype\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000013))**\_\_rt\_lib\_init\_lc\_monetary\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000015))**\_\_rt\_lib\_init\_lc\_numeric\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000017))**\_\_rt\_lib\_init\_lc\_time\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000019))**\_\_rt\_lib\_init\_preinit\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000004))**\_\_rt\_lib\_init\_rand\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000E))**\_\_rt\_lib\_init\_return** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000033))**\_\_rt\_lib\_init\_signal\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000001D))**\_\_rt\_lib\_init\_stdio\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$00000025))**\_\_rt\_lib\_init\_user\_alloc\_1** (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect$$libinit$$0000000C))**\_\_rt\_lib\_shutdown** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown.o(.ARM.Collect$$libshutdown$$00000000))  
  
[Called By]

* + [>>](#3tbugp1)   \_\_rt\_exit\_ls

**\_\_rt\_lib\_shutdown\_cpp\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000002))**\_\_rt\_lib\_shutdown\_fp\_trap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000007))**\_\_rt\_lib\_shutdown\_heap\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$0000000F))**\_\_rt\_lib\_shutdown\_return** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000010))**\_\_rt\_lib\_shutdown\_signal\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$0000000A))**\_\_rt\_lib\_shutdown\_stdio\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$00000004))**\_\_rt\_lib\_shutdown\_user\_alloc\_1** (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect$$libshutdown$$0000000C))**\_\_rt\_entry** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry.o(.ARM.Collect$$rtentry$$00000000))  
  
[Called By]

* + [>>](#1fob9te)   \_\_scatterload\_rt2
  + [>>](#gjdgxs)   \_\_main

**\_\_rt\_entry\_presh\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$00000002))**\_\_rt\_entry\_sh** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry4.o(.ARM.Collect$$rtentry$$00000004))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = \_\_rt\_entry\_sh ⇒ \_\_user\_setup\_stackheap

[Calls]

* + [>>](#2fk6b3p)   \_\_user\_setup\_stackheap

**\_\_rt\_entry\_li** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000A))  
  
[Calls]

* + [>>](#1t3h5sf)   \_\_rt\_lib\_init

**\_\_rt\_entry\_postsh\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$00000009))**\_\_rt\_entry\_main** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000D))  
  
[Stack]

* + Max Depth = 24 + Unknown Stack Size
  + Call Chain = \_\_rt\_entry\_main ⇒ main ⇒ configureOutput

[Calls]

* + [>>](#upglbi)   exit
  + [>>](#zu0gcz)   main

**\_\_rt\_entry\_postli\_1** (Thumb, 0 bytes, Stack size unknown bytes, \_\_rtentry2.o(.ARM.Collect$$rtentry$$0000000C))**\_\_rt\_exit** (Thumb, 0 bytes, Stack size unknown bytes, rtexit.o(.ARM.Collect$$rtexit$$00000000))  
  
[Called By]

* + [>>](#upglbi)   exit

**\_\_rt\_exit\_ls** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000003))  
  
[Calls]

* + [>>](#49x2ik5)   \_\_rt\_lib\_shutdown

**\_\_rt\_exit\_prels\_1** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000002))**\_\_rt\_exit\_exit** (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect$$rtexit$$00000004))  
  
[Calls]

* + [>>](#2szc72q)   \_sys\_exit

**Reset\_Handler** (Thumb, 12 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))**NMI\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
  
[Calls]

* + [>>](#1mrcu09)   NMI\_Handler

[Called By]

* + [>>](#1mrcu09)   NMI\_Handler

[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**HardFault\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
  
[Calls]

* + [>>](#46r0co2)   HardFault\_Handler

[Called By]

* + [>>](#46r0co2)   HardFault\_Handler

[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**SVC\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
  
[Calls]

* + [>>](#2lwamvv)   SVC\_Handler

[Called By]

* + [>>](#2lwamvv)   SVC\_Handler

[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**PendSV\_Handler** (Thumb, 2 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
  
[Calls]

* + [>>](#111kx3o)   PendSV\_Handler

[Called By]

* + [>>](#111kx3o)   PendSV\_Handler

[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**ADC0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**CMP0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DAC0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DMA0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DMA1\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DMA2\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DMA3\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**DefaultISR** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(.text)

**FTFA\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**I2C0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**I2C1\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**LLWU\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**LPTMR0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**LVD\_LVW\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**MCG\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**PIT\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**PORTA\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**RTC\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**RTC\_Seconds\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**Reserved20\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**Reserved39\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**Reserved45\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**SPI0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**SPI1\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**TPM0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**TPM1\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**TPM2\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**TSI0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**UART0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**UART1\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**UART2\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**USB0\_IRQHandler** (Thumb, 0 bytes, Stack size 0 bytes, startup\_mkl25z4.o(.text))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**\_\_user\_initial\_stackheap** (Thumb, 0 bytes, Stack size unknown bytes, startup\_mkl25z4.o(.text))  
  
[Called By]

* + [>>](#2fk6b3p)   \_\_user\_setup\_stackheap

**\_\_aeabi\_uidiv** (Thumb, 0 bytes, Stack size 0 bytes, aeabi\_sdiv.o(.text))  
  
[Called By]

* + [>>](#meukdy)   Init\_SysTick

**\_\_aeabi\_uidivmod** (Thumb, 20 bytes, Stack size 0 bytes, aeabi\_sdiv.o(.text), UNUSED)**\_\_aeabi\_idiv** (Thumb, 0 bytes, Stack size 8 bytes, aeabi\_sdiv.o(.text), UNUSED)**\_\_aeabi\_idivmod** (Thumb, 326 bytes, Stack size 8 bytes, aeabi\_sdiv.o(.text), UNUSED)**\_\_use\_two\_region\_memory** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_rt\_heap\_escrow$2region** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_rt\_heap\_expand$2region** (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)**\_\_user\_setup\_stackheap** (Thumb, 62 bytes, Stack size 8 bytes, sys\_stackheap\_outer.o(.text))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = \_\_user\_setup\_stackheap

[Calls]

* + [>>](#1tuee74)   \_\_user\_perproc\_libspace
  + [>>](#2nusc19)   \_\_user\_initial\_stackheap

[Called By]

* + [>>](#vx1227)   \_\_rt\_entry\_sh

**exit** (Thumb, 16 bytes, Stack size 8 bytes, exit.o(.text))  
  
[Stack]

* + Max Depth = 8 + Unknown Stack Size
  + Call Chain = exit

[Calls]

* + [>>](#19c6y18)   \_\_rt\_exit
  + [>>](#2y3w247)   \_call\_atexit\_fns (Weak Reference)

[Called By]

* + [>>](#4f1mdlm)   \_\_rt\_entry\_main

**\_\_user\_libspace** (Thumb, 8 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)**\_\_user\_perproc\_libspace** (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text))  
  
[Called By]

* + [>>](#2fk6b3p)   \_\_user\_setup\_stackheap

**\_\_user\_perthread\_libspace** (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)**\_sys\_exit** (Thumb, 8 bytes, Stack size 0 bytes, sys\_exit.o(.text))  
  
[Called By]

* + [>>](#nmf14n)   \_\_rt\_exit\_exit

**\_\_I$use$semihosting** (Thumb, 0 bytes, Stack size 0 bytes, use\_no\_semi.o(.text), UNUSED)**\_\_use\_no\_semihosting\_swi** (Thumb, 2 bytes, Stack size 0 bytes, use\_no\_semi.o(.text), UNUSED)**\_\_semihosting\_library\_function** (Thumb, 0 bytes, Stack size unknown bytes, indicate\_semi.o(.text), UNUSED)**Init\_SysTick** (Thumb, 68 bytes, Stack size 8 bytes, systick.o(.text.Init\_SysTick))  
  
[Stack]

* + Max Depth = 8
  + Call Chain = Init\_SysTick

[Calls]

* + [>>](#1302m92)   \_\_aeabi\_uidiv

[Called By]

* + [>>](#zu0gcz)   main

**PORTD\_IRQHandler** (Thumb, 44 bytes, Stack size 0 bytes, main.o(.text.PORTD\_IRQHandler))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**SysTick\_Handler** (Thumb, 20 bytes, Stack size 0 bytes, systick.o(.text.SysTick\_Handler))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(RESET)

**SystemInit** (Thumb, 12 bytes, Stack size 0 bytes, system\_mkl25z4.o(.text.SystemInit))  
[Address Reference Count : 1]

* + startup\_mkl25z4.o(.text)

**configureOutput** (Thumb, 140 bytes, Stack size 16 bytes, main.o(.text.configureOutput))  
  
[Stack]

* + Max Depth = 16
  + Call Chain = configureOutput

[Called By]

* + [>>](#zu0gcz)   main

**main** (Thumb, 128 bytes, Stack size 8 bytes, main.o(.text.main))  
  
[Stack]

* + Max Depth = 24
  + Call Chain = main ⇒ configureOutput

[Calls]

* + [>>](#4iylrwe)   waitSysTickCounter
  + [>>](#1yyy98l)   task2FlashGreen
  + [>>](#3jtnz0s)   task1FlashRed
  + [>>](#2koq656)   configureOutput
  + [>>](#meukdy)   Init\_SysTick

[Called By]

* + [>>](#4f1mdlm)   \_\_rt\_entry\_main

**task1FlashRed** (Thumb, 84 bytes, Stack size 8 bytes, main.o(.text.task1FlashRed))  
  
[Stack]

* + Max Depth = 8
  + Call Chain = task1FlashRed

[Called By]

* + [>>](#zu0gcz)   main

**task2FlashGreen** (Thumb, 84 bytes, Stack size 8 bytes, main.o(.text.task2FlashGreen))  
  
[Stack]

* + Max Depth = 8
  + Call Chain = task2FlashGreen

[Called By]

* + [>>](#zu0gcz)   main

**waitSysTickCounter** (Thumb, 16 bytes, Stack size 0 bytes, systick.o(.text.waitSysTickCounter))  
  
[Called By]

* + [>>](#zu0gcz)   main

Local SymbolsUndefined Global Symbols**\_call\_atexit\_fns** (ARM, 0 bytes, Stack size 0 bytes, UNDEFINED)  
  
[Called By]

* + [>>](#upglbi)   exit